

DEK Topside Reference System (TRS)

The DEK Topside Reference System builds on the DEK Virtual Tooling Panel (VPT) with the addition of singulated substrate thickness tolerances and active surround technology for high-precision results.



DEK Topside Referencing System (TRS) is advanced tooling engineered to handle and align singulated substrates from a carrier to enable mass imaging. Leveraging the concepts of the award-winning Virtual Panel Tooling technology, DEK TRS moves singulated device processing to the next level.

Specifically designed to address assembly complexities as flip chip manufacture moves to smaller pin pitch, thin-core and coreless technologies, DEK TRS provides edge alignment of multiple substrates into a virtual panel to facilitate multi-part printing with a single stroke. DEK TRS has proven its success with processor, SMT, ceramic and sensor applications, though its relevance for mass imaging of challenging products is unlimited.

Features and benefits:

- Enables imaging of singulated substrates in a single print stroke for increased throughput
- Compatible with various carrier types: J-boats, JEDEC tray, Flat Boat and custom carriers
- Effectiveness confirmed on challenging components such as LGA, BGA, 01005 passives and 0.25 mm - 0.3 mm BGA pad size, 0.5 mm pad pitch
- Fast cycle time, typically less than 30 seconds



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